## What is claimed is:

- 1. A method for fabricating a metal structure,
- 2 comprising:
- 3 providing a semiconductor substrate with a conductor
- 4 thereon;
- forming an insulating layer overlying the
- 6 semiconductor substrate;
- forming a hole in the insulating layer exposing the
- 8 conductor;
- 9 substantially filling the hole with a conductive
- 10 material as a conductive plug;
- forming a silicon carbon-containing film;
- forming a low dielectric constant layer;
- forming a trench in the low dielectric constant
- layer and the silicon carbon-containing film;
- forming a lining layer on the trench; and
- 16 substantially filling the trench with copper or
- 17 copper alloy electrically connecting the
- 18 conductive plug.
- 1 2. The method as claimed in claim 1, wherein the
- 2 conductive material comprises tungsten.
- 1 3. The method as claimed in claim 1, wherein the
- 2 conductor comprises metal silicide.
- 1 4. The method as claimed in claim 1, wherein the
- 2 semiconductor substrate comprises silicon.
- 1 5. The method as claimed in claim 1, wherein the
- 2 semiconductor substrate comprises silicon germanium.

thereof.

4

TopTeam

- 1 6. The method as claimed in claim 1, wherein the 2 conductor is composed of doped semiconductor, 3 polysilicon, metal, metal compound or a combination
- 7. The method as claimed in claim 1, wherein the insulating layer comprises undoped silicate glass (USG).
- 1 8. The method as claimed in claim 1, wherein the 2 thickness of the silicon carbon-containing film is less 3 than 500 Å.
- 9. The method as claimed in claim 1, wherein the silicon carbon-containing film is silicon carbide(SiC).
- 1 10. The method as claimed in claim 1, wherein the 2 carbon content of the silicon carbon-containing film 3 exceeds 20%.
- 1 11. The method as claimed in claim 1, wherein the dielectric constant (k) of the low dielectric constant layer is less than 3.0.
- 1 12. The method as claimed in claim 1, wherein the 2 low dielectric constant layer is formed by chemical vapor 3 deposition (CVD) and/or Spin-On method.
- 1 13. The method as claimed in claim 1, wherein the 2 low dielectric constant layer comprises inorganic film 3 and/or organic film.
- 1 14. The method as claimed in claim 1, wherein the width of the hole is less than 950Å.

- 1 15. The method as claimed in claim 1, wherein the width of the trench is less than 1300Å.
- 1 16. The method as claimed in claim 1, wherein the lining layer comprises Ta and/or TaN.
- 1 17. The method as claimed in claim 1, wherein the 2 copper or copper alloy is formed by chemical vapor 3 deposition (CVD) and/or physical vapor deposition (PVD).
- 18. The method as claimed in claim 1, wherein the copper or copper alloy is formed by plating.
- 1 19. A metal structure, comprising:

9

10

11

12

13

14

15

16

17

- 2 a semiconductor substrate with a conductor thereon;
- an insulating layer overlying the semiconductor substrate having a hole therein exposing the conductor;
- a conductive plug substantially filling the hole and
  electrically connecting the underlying
  conductor;
  - a silicon carbon-containing film overlying the insulating layer and the conductive plug;
    - a low dielectric constant layer overlying the silicon carbon-containing film;
  - a trench in the low dielectric constant layer and the silicon carbon-containing film; and
  - a copper or copper alloy conductor substantially filling the trench, electrically connecting the conductive plug.

- 1 20. The structure as claimed in claim 19, wherein 2 the conductive plug comprises tungsten.
- 1 21. The structure as claimed in claim 19, wherein 2 the conductor comprises metal silicide.
- 1 22. The structure as claimed in claim 19, wherein 2 the semiconductor substrate comprises silicon germanium.
- The structure as claimed in claim 19, wherein 1 23. 2 conductor is composed of doped semiconductor, 3 polysilicon, metal, metal compound or combination a 4 thereof.
- 1 24. The structure as claimed in claim 19, wherein 2 the insulating layer comprises undoped silicate glass 3 (USG).
- 1 25. The structure as claimed in claim 19, wherein 2 the thickness of the silicon carbon-containing film is 3 less than 500 Å.
- 1 26. The structure as claimed in claim 19, wherein 2 the silicon carbon-containing film comprises silicon 3 carbide (SiC).
- 1 27. The structure as claimed in claim 19, wherein 2 the carbon content of the silicon carbon-containing film 3 exceeds 20%.
- 1 28. The structure as claimed in claim 19, wherein 2 the dielectric constant (k) of the low dielectric 3 constant layer is less than 3.0.

- 1 29. The structure as claimed in claim 19, wherein 2 the low dielectric constant layer is formed by chemical 3 vapor deposition (CVD) and/or Spin-On method.
- 1 30. The structure as claimed in claim 19, wherein 2 the low dielectric constant layer comprises inorganic 3 film and/or organic film.
- 1 31. The structure as claimed in claim 19, wherein 2 the width of the hole is less than 950Å.
- 1 32. The structure as claimed in claim 19, wherein 2 the width of the trench is less than 1300Å.
- 1 33. The structure as claimed in claim 19, wherein 2 the lining layer comprises Ta and/or TaN.
- 1 34. A metal structure, comprising:
- a semiconductor substrate with a nickel silicide
  thereon;
- an insulating layer overlying the semiconductor

  substrate having a hole therein exposing the

  conductor;
- a conductive plug substantially filling the hole and electrically connecting the underlying conductor;
- a silicon carbon-containing film overlying the insulating layer and the conductive plug;
- a low dielectric constant layer overlying the silicon carbon-containing film;
- a trench in the low dielectric constant layer and the silicon carbon-containing film; and

- a copper or copper alloy conductor substantially
  filling the trench, electrically connecting the
  conductive plug.
  - 1 35 The structure as claimed in claim 34, wherein 2 the conductive plug comprises tungsten.
  - 36. The structure as claimed in claim 34, wherein the thickness of the silicon carbon-containing film is less than 500 Å.
  - 1 37. The structure as claimed in claim 34, wherein 2 the carbon content of the silicon carbon-containing film 3 exceeds 20%.
  - 1 38. The structure as claimed in claim 34, wherein 2 the dielectric constant (k) of the low dielectric 3 constant layer is less than 3.0.
  - 1 39. The structure as claimed in claim 34, wherein the width of the hole is less than 950Å.
  - 1 40. The structure as claimed in claim 34, wherein the width of the trench is less than 1300Å.
  - 1 41. The structure as claimed in claim 34, wherein 2 the lining layer comprises Ta and/or TaN.